ELECTRONIC INFORMATION DISCLOSURE STATEMENT

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Title of Invention

BIPOLAR TRANSISTOR SELF-ALIGNMENT WITH RAISED EXTRINSIC BASE EXTENSION AND METHODS OF FORMING SAME

Application Number :

Confirmation Number:

First Named Applicant:

Gregory Freeman

Attorney Docket Number:

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or 6380017 or 6383855 or 6388307).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	5128271	1992-07-07	BRONNER, ET			
		5 40 4000	1000 00 07	AL.			
	2	5494836	1996-02-27	IMAI			
	3	5506427	1996-04-09	IMAI			
	4	5599723	1997-02-04	SATO			
	5	5648280	1997-07-15	KATO			
	6	5656514	1997-08-12	AHLGREN, ET			
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	7	5668396	1997-09-16	SATO			
	8	5723378	1998-03-03	SATO			
	9	5766999	1998-06-16	SATO			
	10	5789800	1998-08-04	KOHNO			
	11	5798561	1998-08-25	SATO			
	12	5821149	1998-10-13	SCHUPPEN, ET			
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	13	5834800	1998-11-10 J	ALALI-FARAHAN	1,		
				ET AL.			
	14	5846869	1998-12-08	HASHIMOTO,			
				ET AL.			
	15	5962880	1999-10-05	ODA, ET AL.			
	16	6121101	2000-09-19	KING, ET AL.			

	17	6281097	2001-08-28	AOYAMA		
	18	6287929	2001-09-11	KATO		
	19	6329698	2001-12-11	KOSCIELNIAK, ET AL.		
	20	6337251	2002-01-08	HASHIMOTO		
	21	6346453	2002-02-12	KOVACIC, ET AL.		
	22	6380017	2002-04-30	DARWISH, ET AL.		
	23	6383855	2002-05-07	GAO, ET AL.		
	24	6388307	2002-05-14	KONDO, ET AL.		

Signature

Examiner Name	Date